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## APPLICANTS

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Isao Ogura, Ogaki-shi, JAPAN; *(initials)*\*\* CONTINUING DATA \*\*\*\*\* *(initials)*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *(initials)*JAPAN 11-372781 12/28/1999 YES *(initials)*

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>(initials)</i> Initials				

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## TITLE

Semiconductor integrated circuit related to a circuit operating on the basis of a clock signal

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